

IN THE UNITED STATES PATENT OFFICE

Applicant: Brian T. Brunn et al.
Assignee: Xilinx, Inc.
Title: BIT-EDGE ZERO FORCING EQUALIZER
Serial No.: 10/791,924 File Date: March 2, 2004
Examiner: Siu M. Lee Art Unit: 2611
Docket No.: X-1549 US Conf. No.: 3818

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AMENDMENT IN RESPONSE TO THIRD OFFICE ACTION

In response to the third Office action mailed from the Patent Office on May 13, 2008, please enter the amendments to the claims as indicated in the following claim listing and entertain the ensuing remarks.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.